



IFW 2816

PATENT
ATTORNEY DOCKET NO.: 052593-5003-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Noriaki HIRAGA

Confirmation No.: 3808

Application No.: 10/642,345

Group Art Unit: 2816

Filed: August 18, 2003

Examiner: Nguyen, L.

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE WITH
ENHANCED RESISTANCE TO ELECTROSTATIC BREAKDOWN

Commissioner for Patents
U.S. Patent and Trademark Office
2011 South Clark Place
Customer Window, **Mail Stop Amendment**
Crystal Plaza Two, Lobby, Room 1B03
Arlington, VA 22202

RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT

In response to the Notice of Non-Compliant Amendment dated August 30, 2004, Applicant respectfully resubmits herewith in its entirety the corrected amendments to the specification section of the Preliminary Amendment filed on August 18, 2003 in the above-identified application that provides reference back to the parent patent application.

If there are any additional fees due in connection with this Response, the Commissioner is hereby authorized to charge any additional fees due to Deposit Account No. 50-0310. If a fee is required for an extension of time under 37 C.F.R. 1.136, not accounted for above, such an extension is requested and the fee should also be charged to Deposit Account No. 50-0310.

Respectfully submitted,

MORGAN, LEWIS & BOCKIUS LLP

By: _____

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Dated: September 30, 2004

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REVISED PRELIMINARY AMENDMENT

Sir:

In Response to the Notice of Non-Compliant Amendment dated August 30, 2004, prior to the examination of the above-identified Application on the merits, please amend this Application based upon the following: